

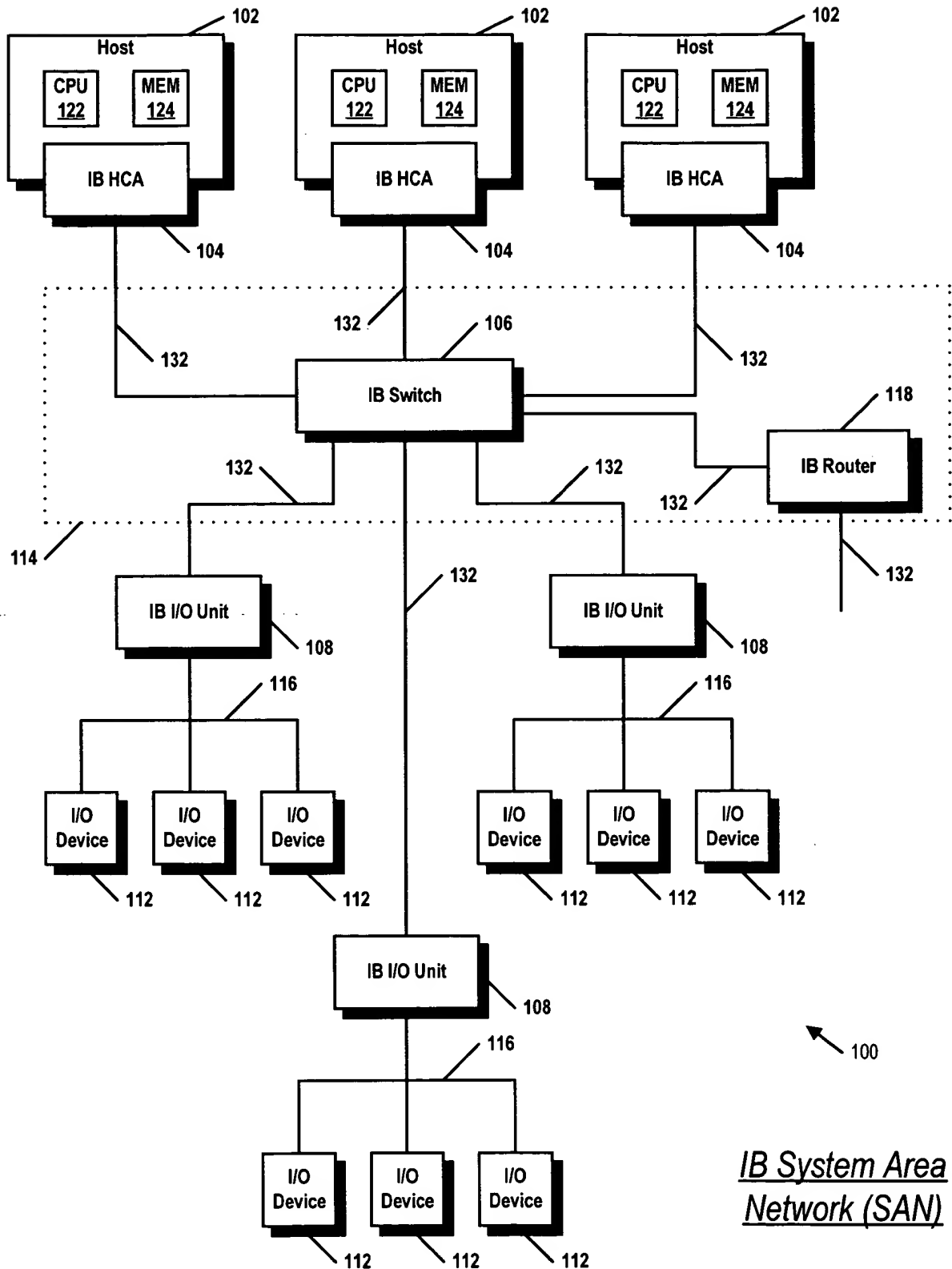
### **Amendments to the Drawings**

Formal Drawings are submitted to the Official Draftsman, a copy of which is attached hereto.



Replacement Sheet

FIG. 1

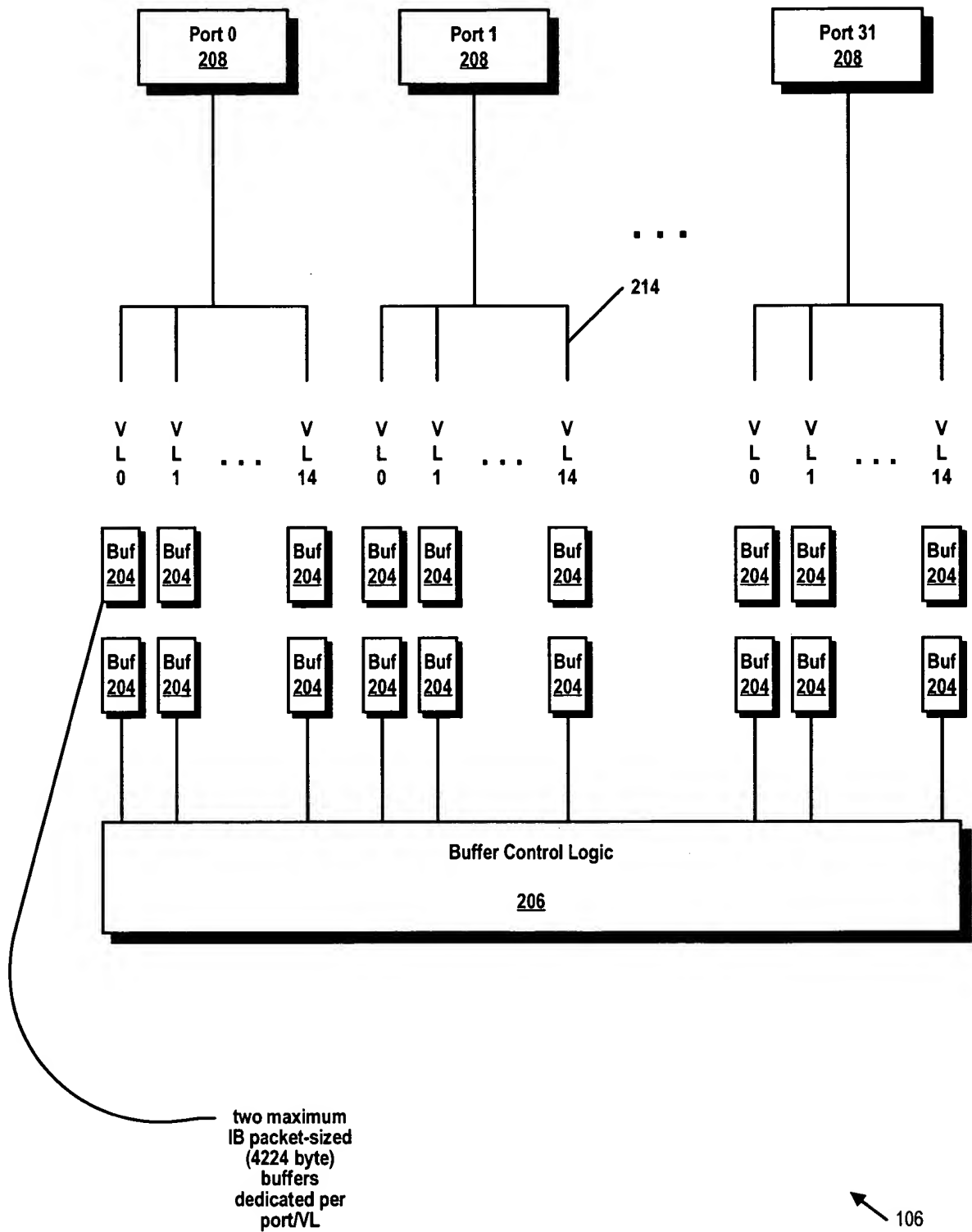


IB System Area  
Network (SAN)



# Replacement Sheet

FIG. 2 (Prior Art)

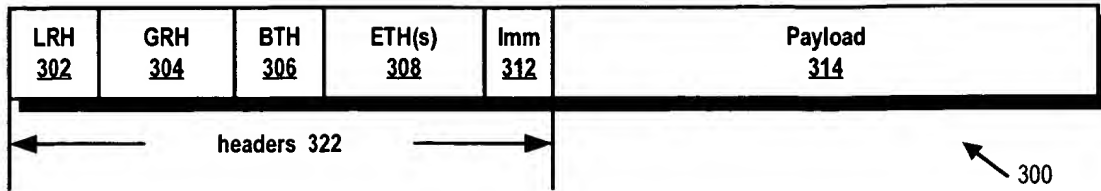


Conventional IB Switch



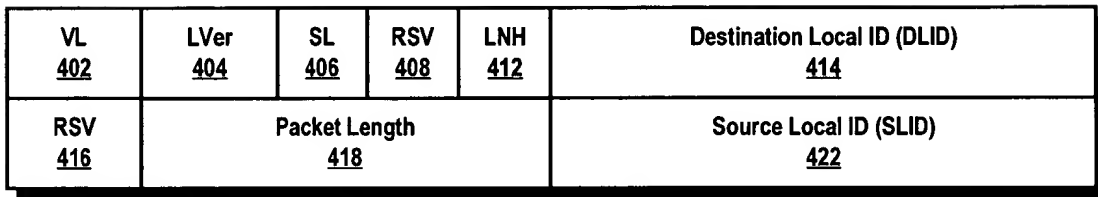
## Replacement Sheet

FIG. 3 (Prior Art)



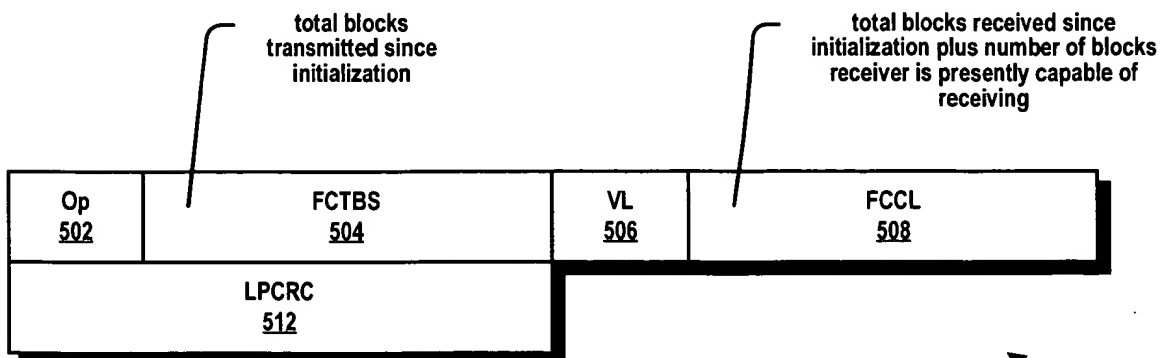
IB Data Packet

FIG. 4 (Prior Art)



Local Routing Header

FIG. 5 (Prior Art)

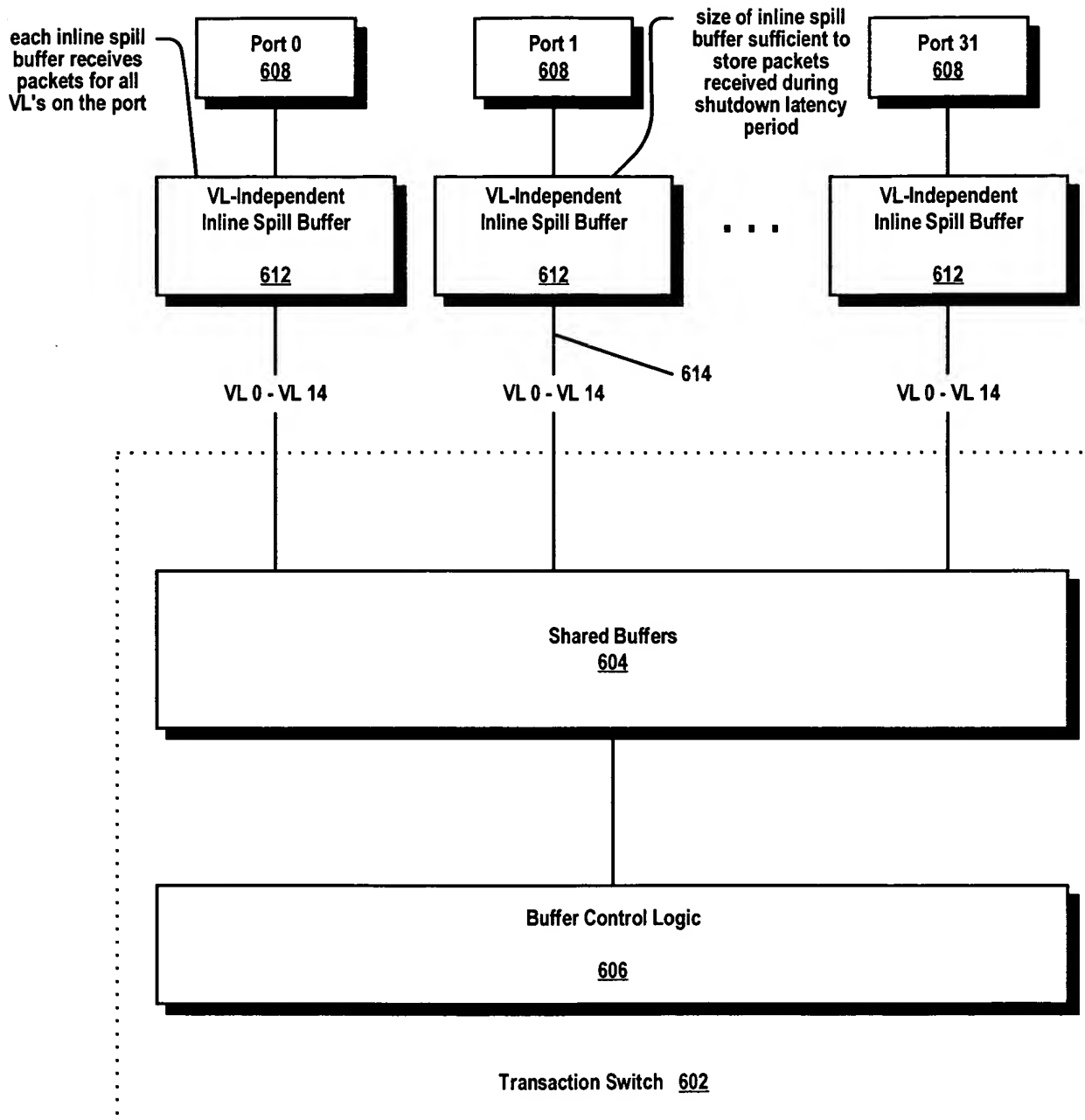


Flow Control Packet



Replacement Sheet

FIG. 6



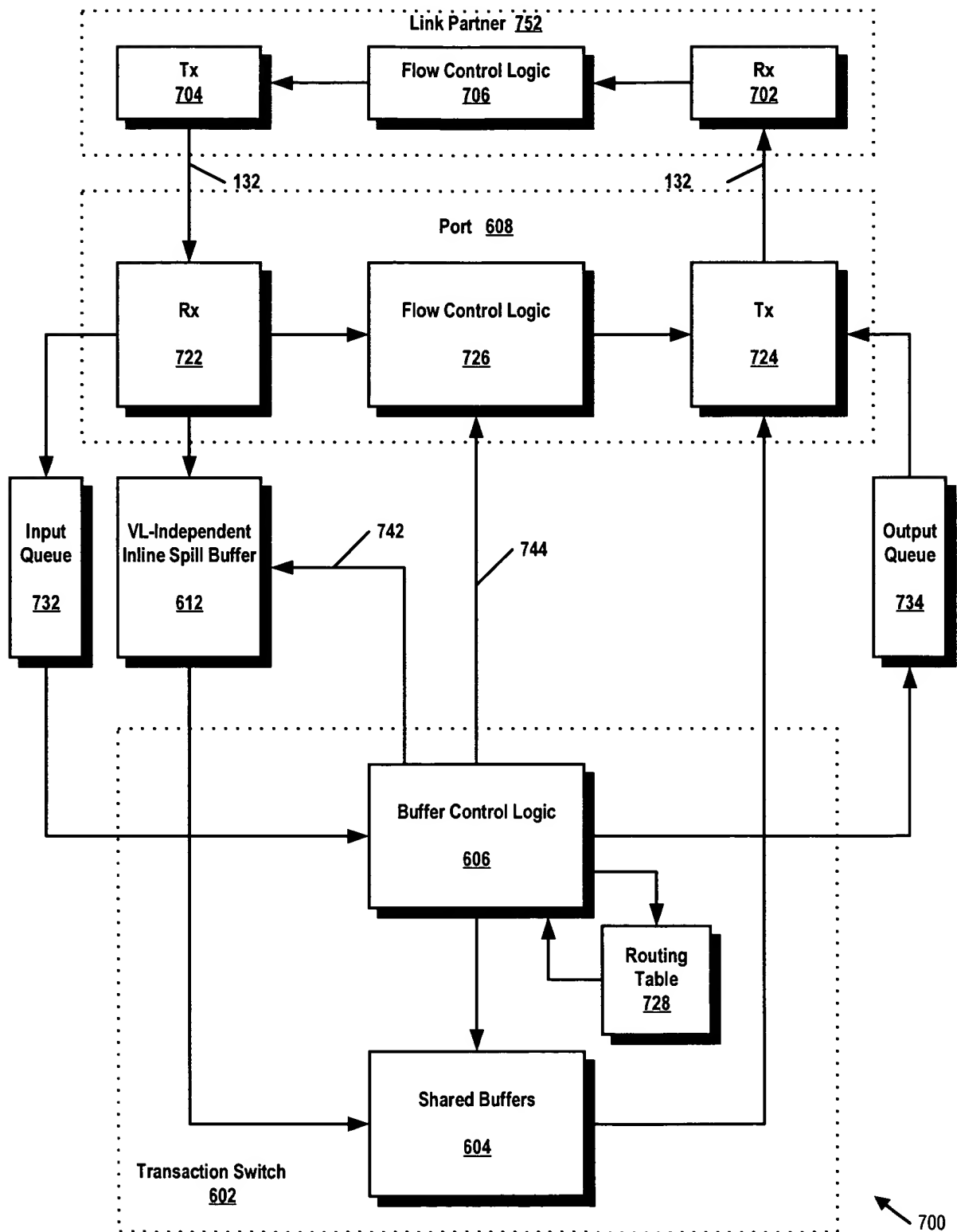
106

IB Switch Capable of Over-Advertising Buffering Resources Using  
Inline Spill Buffer



# Replacement Sheet

FIG. 7



Packet Buffering System Using Inline Spill Buffer



Replacement Sheet

FIG. 8



Valid	Good Packet	VL	GRH present	DLID	SLID	Packet Length	Destination QP
802	804	806	808	812	814	816	818

Input Queue Entry

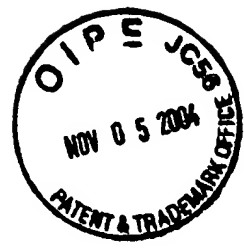


FIG. 9

Tag	VL	Packet Length	Chunk Address 0	Chunk Address 1	Chunk Address 2	Chunk Address 3	Chunk Address 4	Chunk Address 5
902	904	906	908	912	914	916	918	922

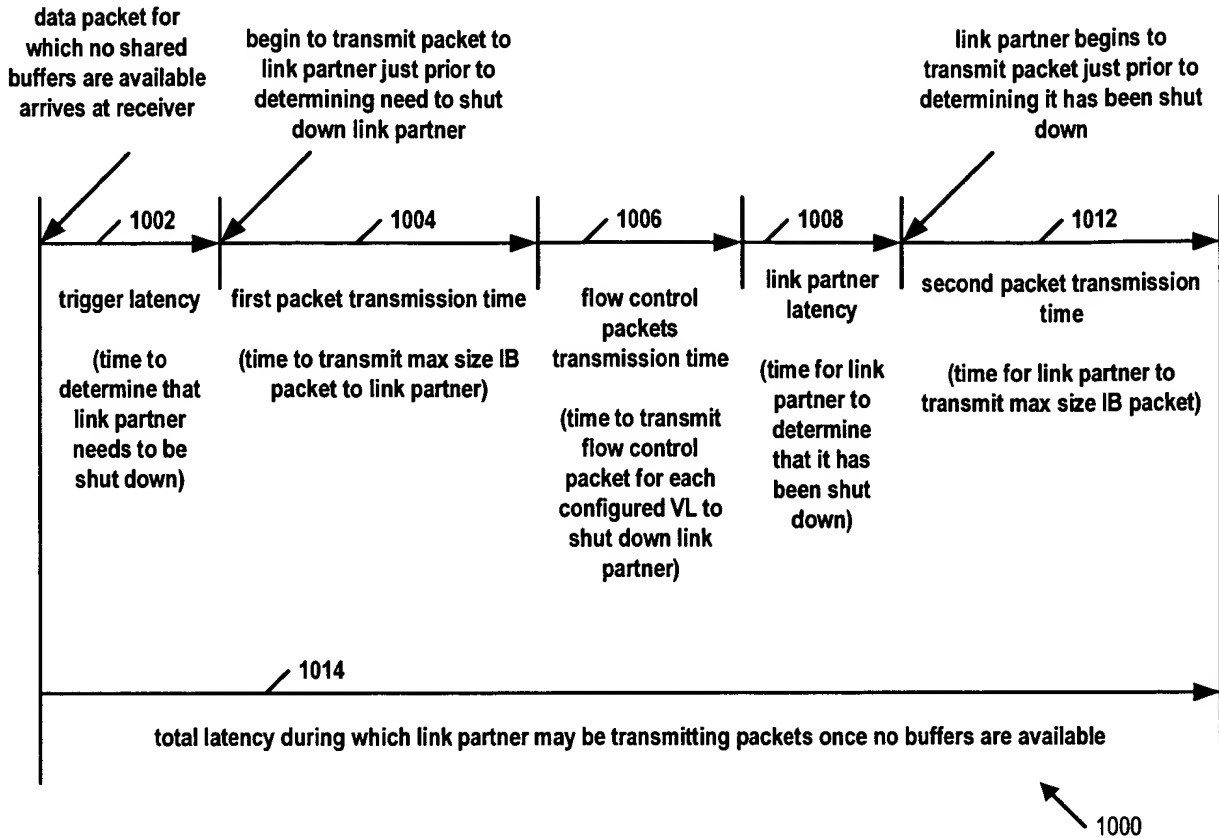
Output Queue Entry





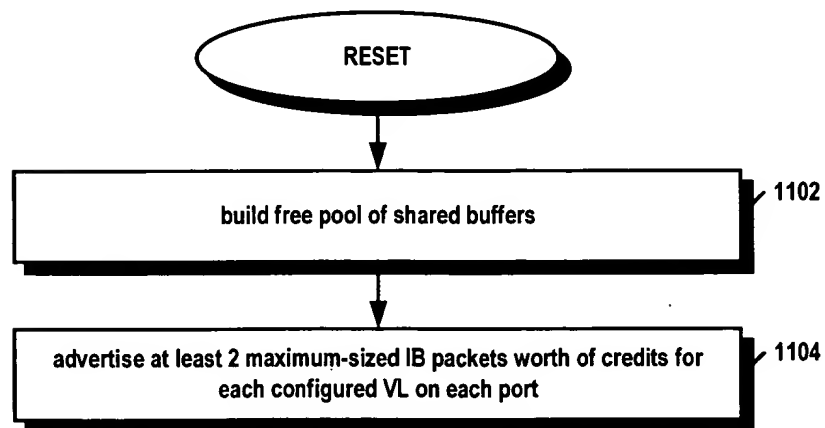
## Replacement Sheet

FIG. 10



Timing Diagram For Determining Shutdown Latency

FIG. 11



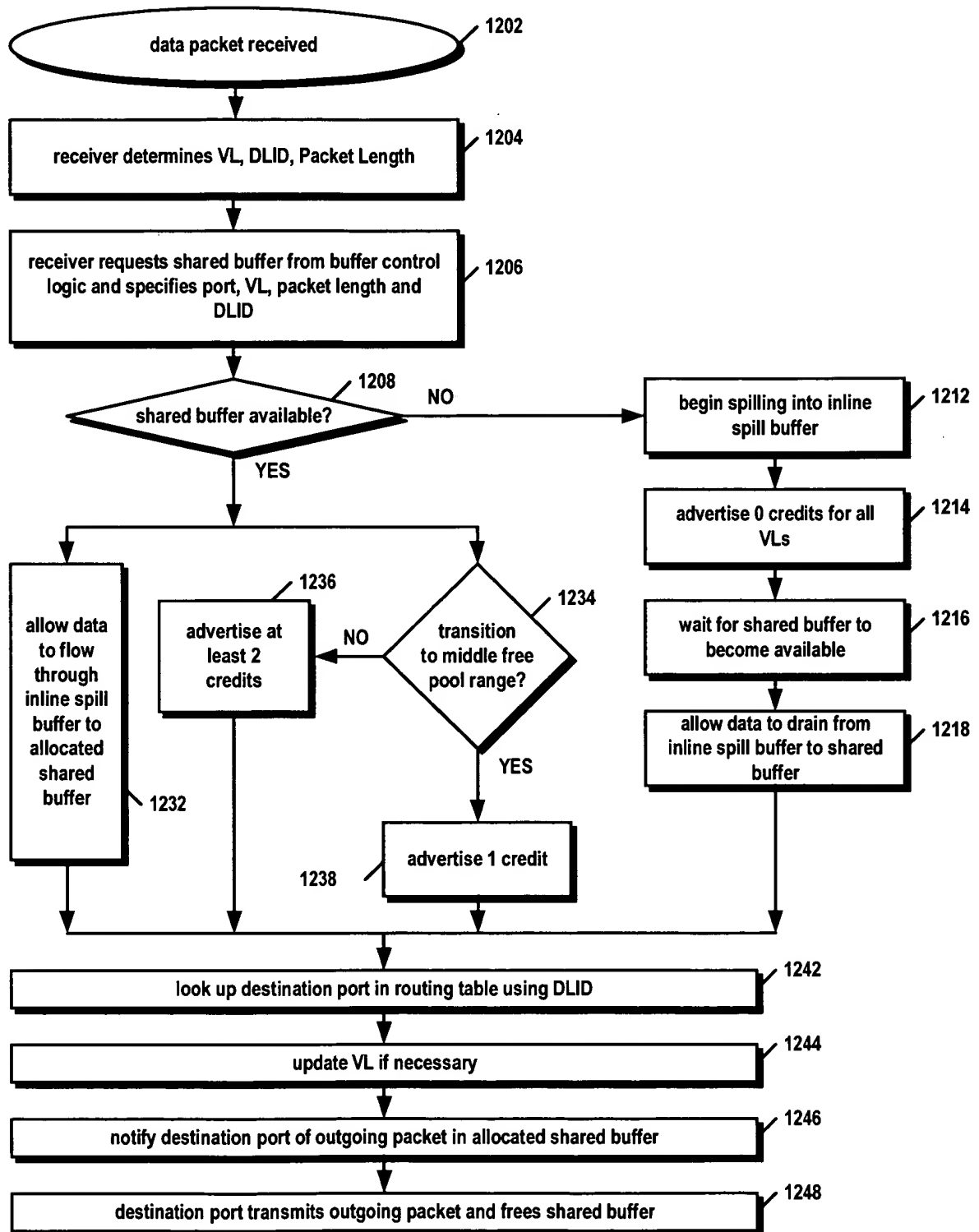
Initialization





## Replacement Sheet

FIG. 12



Over-Advertising Flow Control



## Replacement Sheet

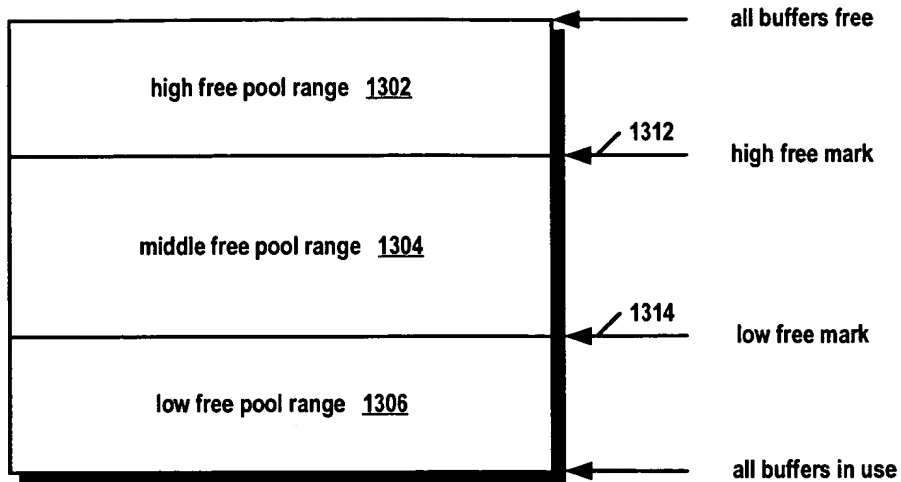


FIG. 13

## Shared Buffer Free Pool Ranges

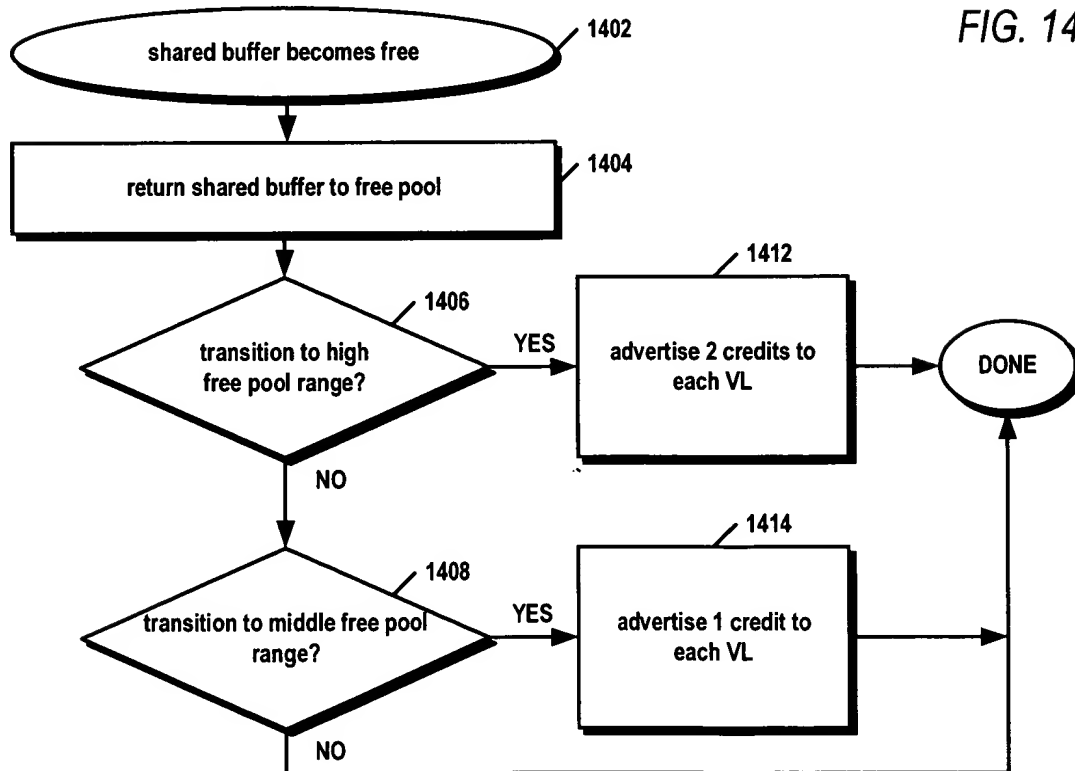


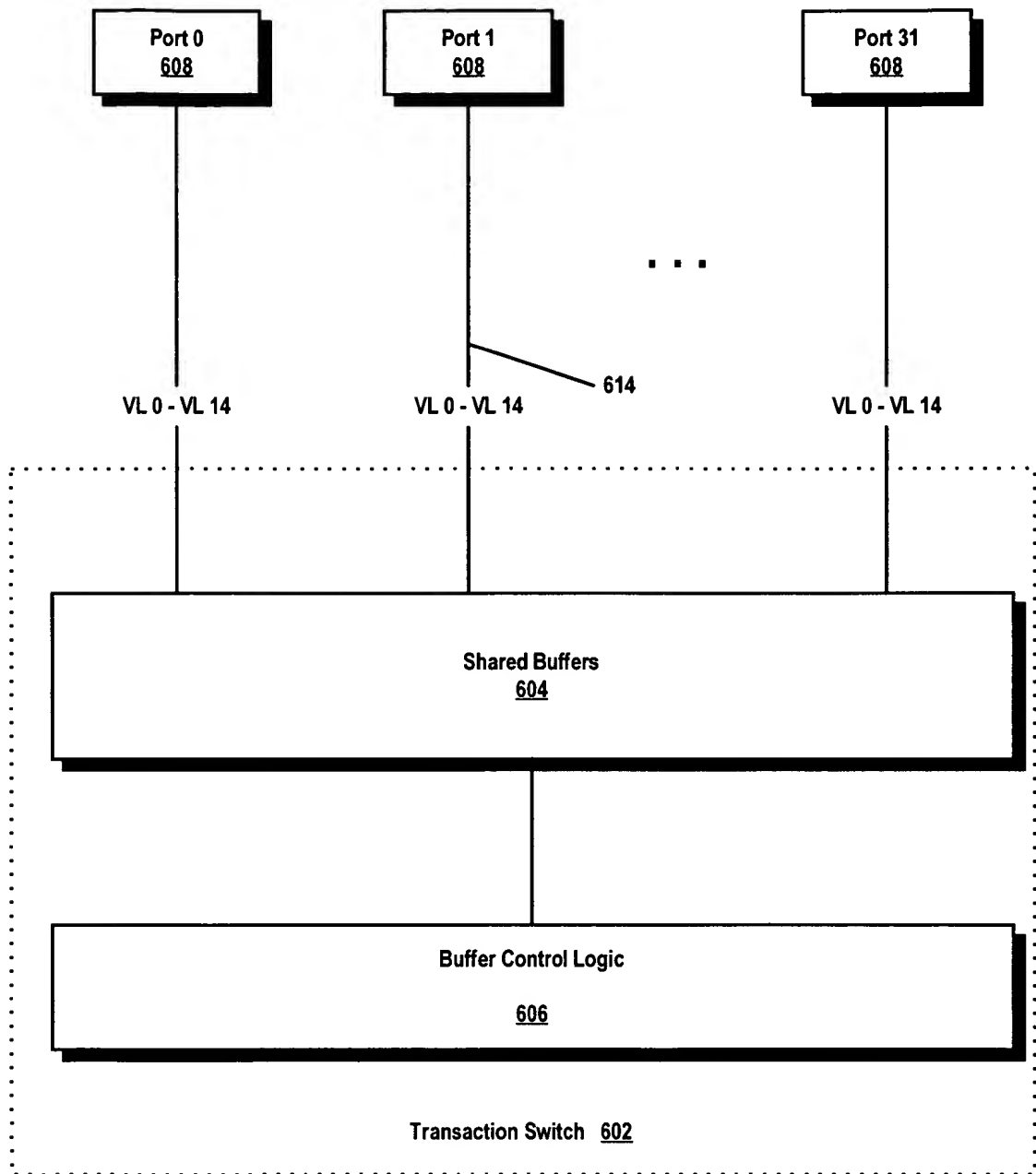
FIG. 14

## Shared Buffer Free Action



# Replacement Sheet

FIG. 15



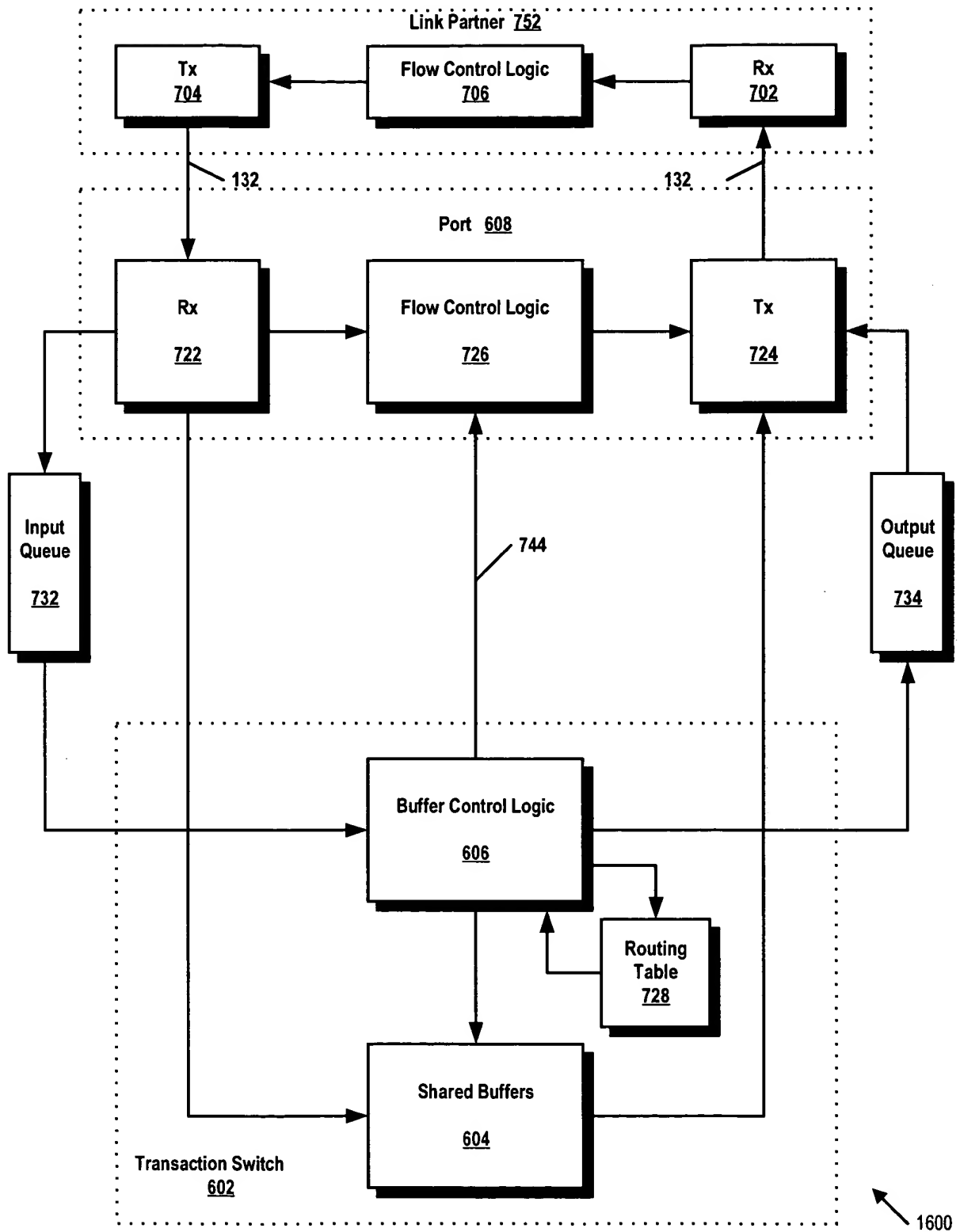
106

IB Switch Capable of Over-Advertising Buffering Resources Without  
Inline Spill Buffer



Replacement Sheet

FIG. 16

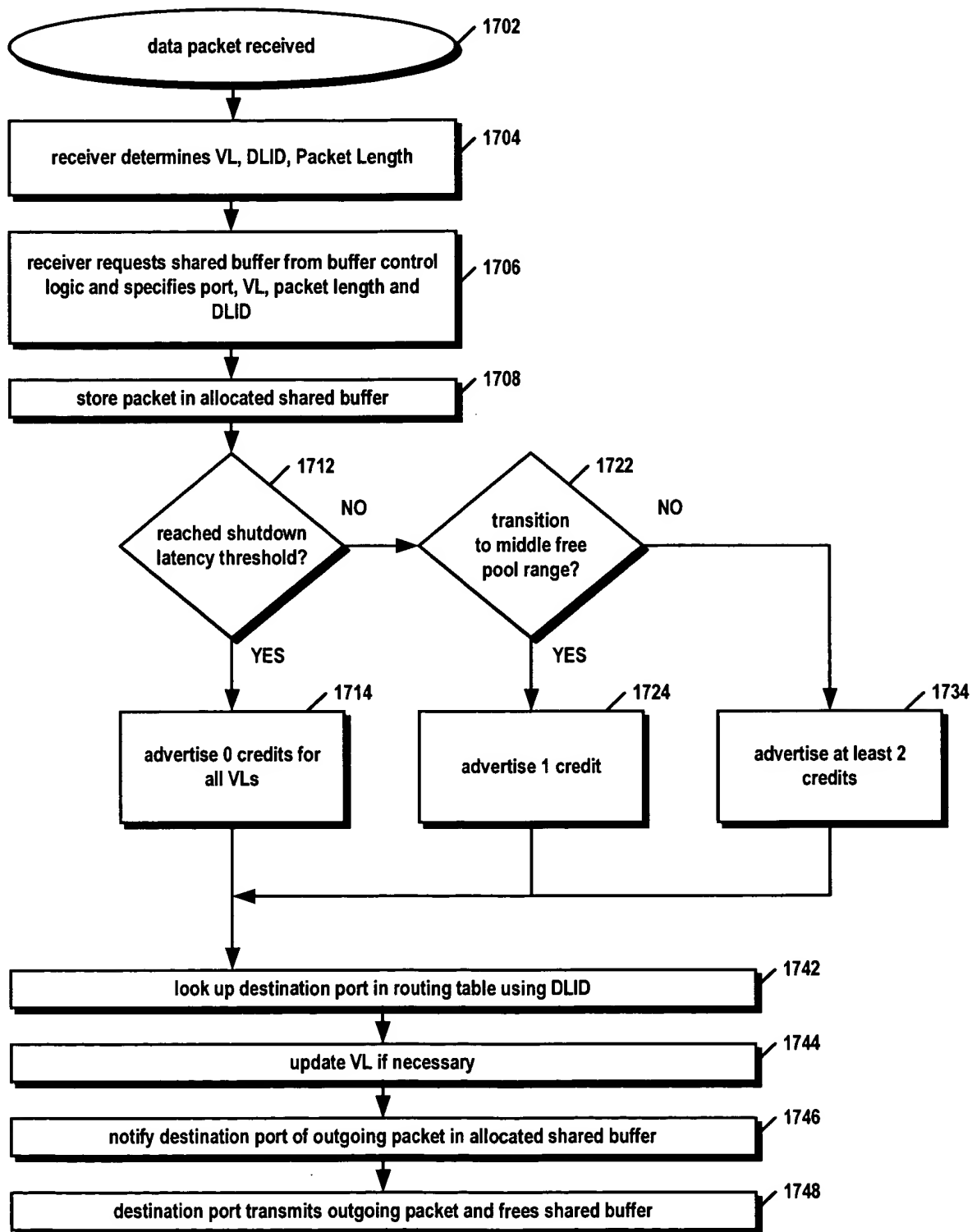


Packet Buffering System Without Inline Spill Buffer



## Replacement Sheet

FIG. 17



Over-Advertising Flow Control



## Replacement Sheet

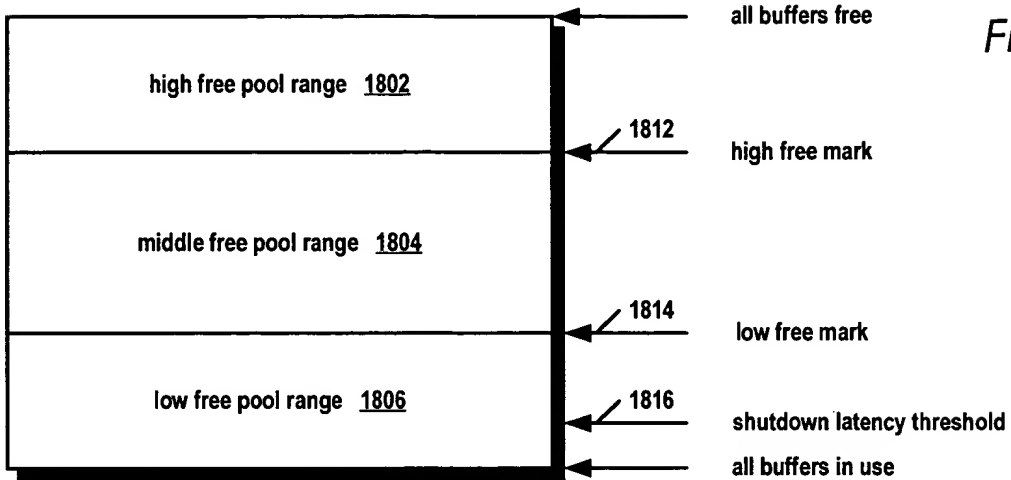


FIG. 18

Shared Buffer Shutdown Latency Threshold